



AF *2m*

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 5415

GOTOH et al.

Art Unit: 2857

Application No.: 10/082,055

Examiner: Edward Raymond

Filed: February 26, 2002

Attorney Dkt. No.: 100021-00074

For: TEST CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT
EFFECTIVELY CARRYING OUT VERIFICATION OF CONNECTION
OF NODES

AMENDMENT UNDER 37 C.F.R. § 1.116

Introductory Comments

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

March 30, 2005

Sir:

In response to the Office Action dated December 30, 2004, please amend the
above-identified application as set forth below.

fee only

BEST AVAILABLE COPY

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